17C Amd+ SDavis 3/5/03

Manufaction Serial No.	09/653,156
Filing Date	August 31, 2000
	Vishnu K. Agarwal, et a
Assignee	Micron Technology, Inc.
Group Art Unit	2813
Examiner	Y. Huynh
	MI22-1518
Title: Enhanced Surface Area Canacitor F	abrication Methods (as amended)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

RESPONSE TO OCTOBER 15, 2002 OFFICE ACTION

To:

Art Unit 2813

Assistant Commissioner for Patents

Washington, D.C. 20231

From:

James E. Lake (Tel. 509-624-4276; Fax 509-838-3424)

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AMENDMENTS

In the Specification

Please replace the title with the following clean replacement title in accordance with 37 C.F.R. §1.121(b)(1)(ii):

-- Enhanced Surface Area Capacitor Fabrication Methods--

In the Claims

Please add new claim 27 in accordance with 37 C.F.R. 1.121(c)(1)(i). No marked up version is required.

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(new) The method of claim 1 wherein the TiN forms a continuous layer within the

first electrode.

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